Total No. of Questions: 8]	9	SEAT No. :	
P-9701		[Total No. of Pages	: 2

## [6179]-244A

## S.E. (Computer Engineering) DIGITAL ELECTRONICS AND LOGIC DESIGN

(2019 Pattern) (Semester - III) (210245)

		(201) 1 attern) (Semester - 111) (210243)	
Time	$e: 2^{j}$	[Max. N	Marks: 70
Insti	ructi	tions to the candidates :	
	1)	Solve Q1 or Q2, Q3 or Q4, Q5 or Q6, Q7 or Q8.	
	<i>2</i> )	Neat diagrams must be drawn wherever necessary.	
	3)	Assume switable data, if necessary.	
<b>Q1</b> )	a)	What are the advantages of M S JK flip flop? Explain the w MS J-K flip flop in detail.	orking of [6]
	b)	State different types of shift registers. Give its applications.	[6]
	c)	Draw and explain 3-bit asynchronous down counter usin flop. Also draw the necessary timing diagram.	g JK flip <b>[6]</b>
<b>Q</b> 2)	a)	OR Compare synchronous and Asynchronous counter.	[6]
	b)	Explain how JK flip is converted into.  i) D FF  ii) T FF	.[6]
	c)	Write short note on Ring counter.	[6]
Q3)	a)	Draw and explain the general structure of PIA.	[6]
	b)	Implement following Boolean function using PAL	[6]
		$F(A, B, C, D) = \Sigma m(0, 1, 3, 15)$	
	c)	Draw the state diagram, state table, and ASM chart for a 2-counter having one enable line E such that $E = 1$ counting en $E = 0$ counting disabled.	•

OR

<i>Q4</i> )	a)	What is an ASM Chart? Name the elements of an ASM chart and define each of them. [6]
	b)	Implement BCD to Ex-3 code converter using PAL. [6]
	c)	What is the difference between PAL and PLA. [5]
<b>Q</b> 5)	a)	With the help of a neat diagram, explain the working of two - input TTL NAND gate. [6]
	b)	Define the following terms and mention the standard values for TTL logic Family. [6]
		i) Power dissipation
		ii) Noise margin
		iii) Propagation Delay
	c)	Draw and explain the circuit diagram of CMOS inverter. [6]
		OR
<b>Q6</b> )	a)	Compare TTL and CMOS logic family. [6]
	b)	What is logic family? Give the classification of logic family and also write important characteristics of CMOS. [6]
	c)	Explain the wired logic output of TTL with neat diagram. [6]
<b>Q</b> 7)	a)	What is system bus? Draw microprocessor bus structure and explain in brief.  [6]
	b)	Write a short note on following with respect to microprocessor. [6]
		i) Address Bus
		ii) Data Bus
		iii) Control Bus
	c)	Explain the Memory organization of the microprocessor. [5]
		OR
<b>Q8</b> )	a)	What is microprocessor? List different applications of microprocessor.
		[6]
	b)	Write a short note on ALU IC 74181. [6]
	c)	With the help of a block diagram explain the fundamental units of a microprocessor. [5]

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